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Patentanmeldung Nr. Patent application No. Demande de brevet n°

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Der Präsident des Europäischen Patentamts;
Im Auftrag

For the President of the European Patent Office
Le Président de l'Office européen des brevets
p.o.

R C van Dijk





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Anmelder/Applicant(s)/Demandeur(s):

ALCATEL
54, rue la Boétie
75008 Paris
FRANCE

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(Falls die Bezeichnung der Erfindung nicht angegeben ist, siehe Beschreibung.
If no title is shown please refer to the description.
Si aucun titre n'est indiqué se referer à la description.)

Clock recovery for strongly distorted eyes

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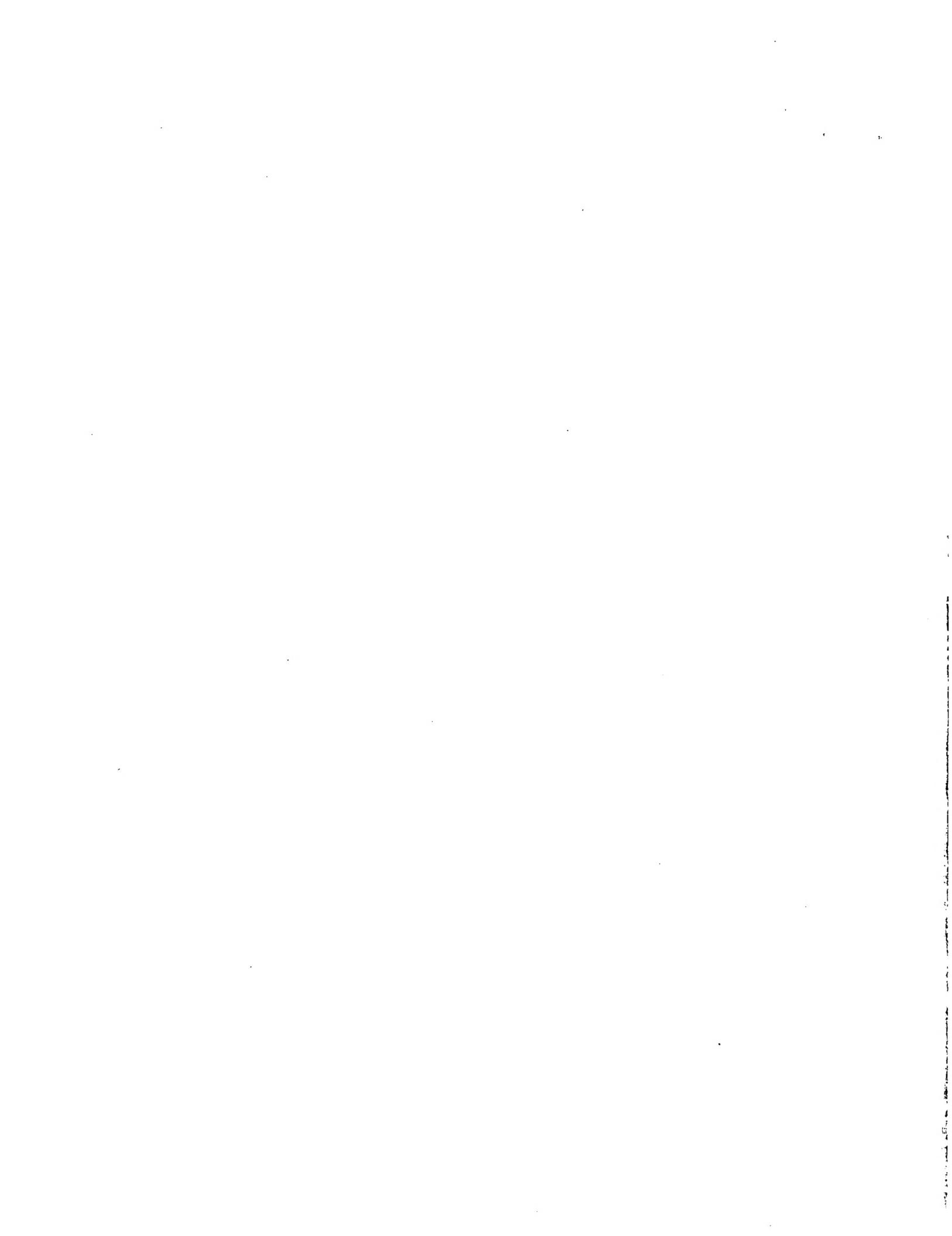
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Applicant:

Alcatel

54 Rue la Boétie

5 75008 Paris

France

Representative:

10 Kohler Schmid + Partner

Patentanwälte GbR

Ruppmannstr. 27

70565 Stuttgart

Germany

15

Clock recovery for strongly distorted eyes

20 The invention relates to a receiver device for optical data signals, in particular optical data signals in the Gb/s range, comprising an opto-electrical conversion unit, a frequency multiplicator unit for frequency-multiplying the converted electrical data signal, and a clock recovery unit.

25

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A receiver device of such type is disclosed in G. P. Agrawal, Fiber-Optic Communication Systems, 2nd edition, Wiley, New York, 1997, page 159-160.

Devices for reading out optical data signals consist of an opto-electric
5 conversion unit, a decision circuit and a clock recovery circuit. The clock recovery circuit's purpose is to isolate the spectral component at the bit rate frequency B. In return to zero (RZ) signals, the data signal contains a clock line, at the bit rate frequency, and the clock line can be isolated by appropriate band pass filtering. The clock line is used for timing the decision circuit.

10 In non return to zero signals (NRZ), the data signal does not contain a clock line at the bit rate frequency. In this case, a frequency doubling (sometimes referred to as squaring) is performed with the received optical signal after its conversion into an electric signal. The spectral components originally symmetrically around
15 half the bit rate frequency B/2 are used as the clock line then. A similar procedure can be performed with RZ signals if the clock line in the original signal is too weak for some reason.

When transmitting high bit rate optical signals in optical fibers over long
20 distances, the optical signal is subject to dispersion, in particular polarization mode dispersion (PMD) and chromatic dispersion (CD). These dispersion effects reduce the optical power within the transmitted optic signal, in particular in the frequency range of half the bit rate B/2. As a result, after frequency doubling, the clock line at the bit rate frequency B is very weak, often too weak
25 for a reliable data recovery. For this reason, expensive dispersion compensation equipment has to be applied in order to improve the clock line generation.

It is the object of this invention to introduce a receiver device for optical data
30 signals, in particular NRZ signals, which has an improved tolerance on dispersion, in particular PMD, as far as generating a clock line is concerned.

This object is achieved, in accordance with the invention, by a receiver device for optical data signals as introduced in the beginning, characterized in that the frequency multiplicator unit performs a frequency multiplication by a factor of n,
5 with n being a natural number larger than 2.

The low frequency part of the optical signal is hardly susceptible to dispersion and maintains its optical power at a relatively high level, i.e. power fluctuations are only minimal. By the frequency multiplication process, this high power part
10 of the optical signal can be projected to higher frequencies and used for generating clock lines at these higher frequencies.

Surprisingly, a clock signal at exactly the bit rate frequency can be generated not only by frequency doubling (n=2), but with any higher multipliers n of the
15 natural series, too. The part of the frequency spectrum of the optical signal which is responsible for the generation of a clock line at the bit rate frequency B is a frequency region symmetrically around B/n. PMD effects attenuate the optical power of the optical signal (and thus the electrical signal) above all in a frequency region at half the bit rate B/2. Already with n=3, a considerable boost
20 in the intensity of the generated clock line at the bit rate frequency B can be achieved due to the decreasing effect of dispersion at lower frequencies of B/3 and below.

In conventional frequency doubling, a bit series of "01 01 01" is used for
25 generating the clock line at the bit rate frequency B. Dispersion can distort this series easily into a "½½ ½½ ½½" series, from which no clock line can be generated any more. According to the invention, a longer bit series comprising at least 3 bits is applied for generating the clock line, which is much more difficult to distort to an equally drastic extent.

A preferred embodiment of the inventive receiver device is characterized in that the receiver device comprises a frequency filter for the spectral power of the electrical data signal, wherein the frequency filter transmits symmetrical around B/n , wherein B is the bit rate of the electrical data signal. Since the decisive 5 frequency range for generating the clock line at the bit rate frequency B is the broadband neighborhood of the frequency B/n , unused parts of the electrical data signal can be neglected during frequency multiplication, thus,easing the multiplication process and increasing the efficiency of the receiver device.

10 In a highly preferred embodiment, n is 4. A quadrupling of the frequency is relatively easy to perform, and the attenuation of the spectral power at $B/4$ due to PMD is only marginal. In this embodiment, a bit series of 0011 generates the clock line at the bit rate frequency after multiplication.

15 Also preferred is an embodiment of the inventive receiver device wherein the optical data signals are Gb/s signals, in particular 10 Gb/s signals or 40 Gb/s signals. Dispersion effects are particularly strong at these high bit rates, so the advantages of the invention are particularly relevant.

20 In another embodiment, the clock recovery unit comprises a phase locked loop (PLL) circuit. The PLL circuit is a standard element, operating up to high bit rates.

In an alternative embodiment, the clock recovery unit comprises a filter clock 25 recovery circuit.

Also within the scope of the invention is a data transmission system comprising an optical transmission link, in particular an optical fiber system, wherein the optical transmission link has a significant dispersion, and an inventive receiver 30 device as described above. In case of a significant dispersion in the optical transmission link, e.g. an attenuation of the optical power of more than 90% at

B/2 compared with the optical power at B/4, the inventive receiver device is particularly useful and is the only known way to generate a sufficiently strong clock line at the bit rate frequency.

5 Also within the scope of the invention is a computer software for generating a clock signal out of an electrical data signal, in particular out of an electrical signal in the Gb/s range, wherein the electrical data signal is subjected to a, frequency multiplication by a factor of n, with n being a natural number larger than 2, in particular n=4.

10 Further advantages can be extracted from the description and the enclosed drawing. The features mentioned above and below can be used in accordance with the invention either individually or collectively in any combination. The embodiments mentioned are not to be understood as exhaustive enumeration
15 but rather have exemplary character for the description of the invention.

The invention is described in the drawings.

Fig. 1 shows the power of a dispersion-affected optical signal as a
20 function of frequency;

Fig. 2 shows the schematic setup of an embodiment of an inventive
 receiver device;

25 Fig. 3 shows the power of an electric signal as a function of frequency as
 generated by the setup of Fig. 2.

Fig. 1 shows the power spectrum 1 of a typical NRZ optical data signal of a bit rate of 10 Gb/s. The power p is indicated on a logarithmic scale versus the
30 frequency f. At low frequencies, the power (per frequency interval) is almost constant, in particular around 2.5 GHz, and up to about 4 GHz. At 5 GHz, i.e. at

half the bit rate frequency of 10 GHz, the power spectrum has a sharp local minimum due to polarization mode dispersion (PMD) losses.

5 The optical power also decreases as a function of frequency in general, making it impossible to filter out a clock line at 10 GHz directly.

A part of the spectrum suitable for a frequency multiplication in accordance with the invention is marked within a rectangular box 2. The power spectrum of an optical signal not subject to PMD is marked with reference numeral 3.

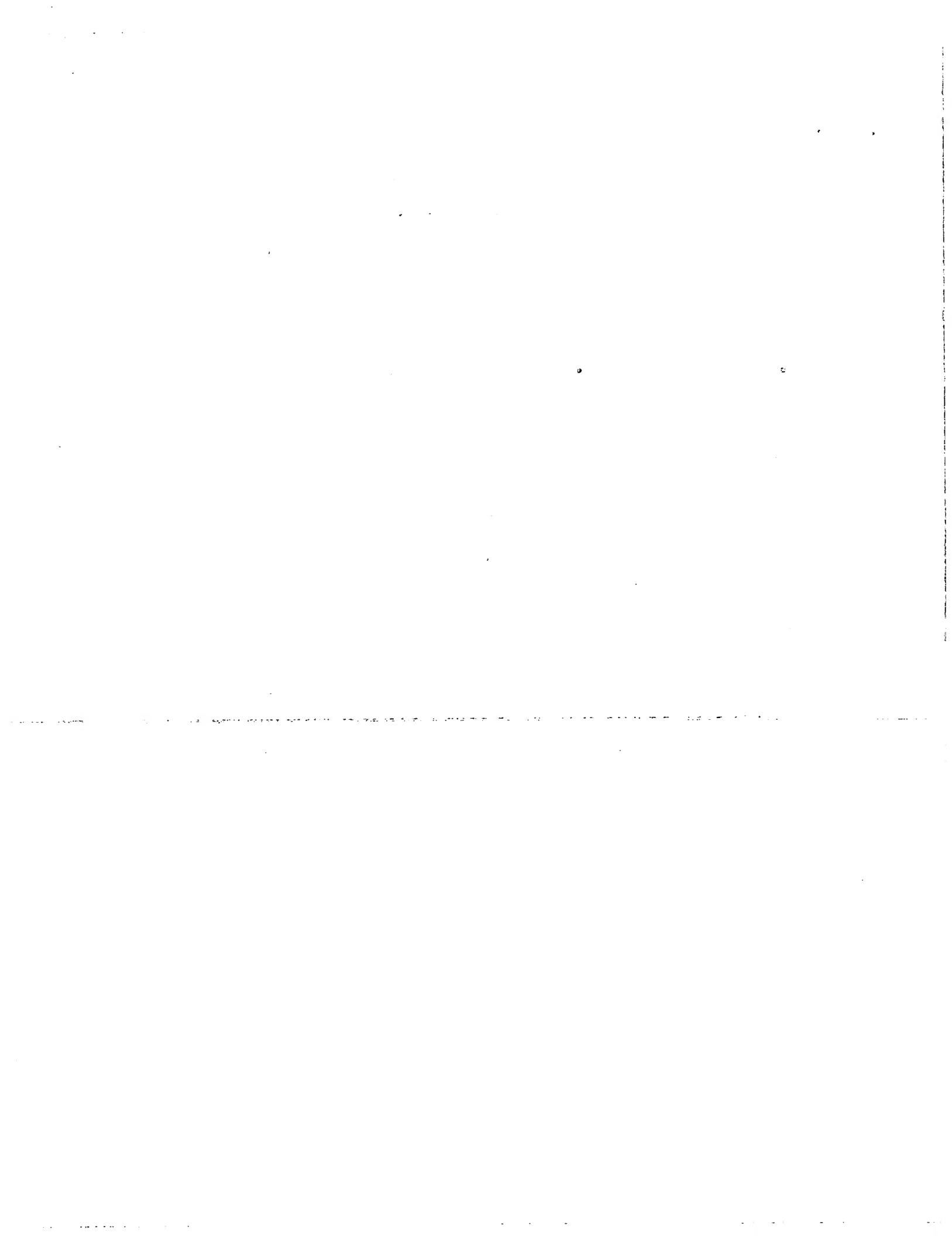
10 Fig. 2 shows the functional units of an inventive receiver device for optical data signals. After an opto-electronic conversion not shown in Fig. 2, an electrical data signal 21 is fed into a frequency filter 22, filtering out all frequency parts of the electric data signal higher than 1/2 th of the bit rate B. In case of the 10 Gbit/s signal of Fig. 1, all frequency parts higher than 5 GHz are filtered out. A 15 filtered electrical data signal 23 is fed into a frequency multiplicator unit 24, multiplying the filtered electrical data signal 23 by a factor of 4.

For this multiplication process, the spectral parts of the data signal 20 symmetrically around $\frac{1}{4}$ of the bitrate frequency are multiplied times 4 generating a clock line at bitrate frequency. The highest frequency spectral parts for multiplication are at half of bitrate frequency.

The multiplied electric data signal 25 is fed into a PLL circuit 26 which in turn generates a clock signal 27 consisting of pulses of a frequency of the bit rate B, here 10 GHz. 25

Fig. 3 shows the power spectrum 31 of the multiplied electrical data signal marked with reference numeral 25 in Fig. 2. The power p is indicated on a logarithmic scale versus the frequency f. Independent of the frequency, the 30 multiplied electrical data signal has a high and almost constant power (per frequency interval) around the clock frequency. In particular, a clock line at 10

GHz is well detectable and can be singled out by an appropriate band pass filter. The intensity of the clock line is almost independent of the dispersion of the original optical signal.



Patent claims

5 1. Receiver device for optical data signals, in particular optical data signals in the Gb/s range, comprising an opto-electrical conversion unit, a frequency multiplicator unit (24) for frequency-multiplying the converted electrical data signal, and a clock recovery unit,

10 characterized in that

15 the frequency multiplicator unit (24) performs a frequency multiplication by a factor of n , with n being a natural number larger than 2.

20 2. Receiver device according to claim 1, characterized in that the receiver device comprises a frequency filter (22) for the spectral power of the electrical data signal, wherein the frequency filter (22) transmits around B/n , wherein B is the bit rate of the electrical data signal.

25 3. Receiver device according to claim 1, characterized in that $n=4$.

30 4. Receiver device according to claim 1, characterized in that the optical data signals are Gb/s signals, in particular 10 Gb/s signals or 40 Gb/s signals.

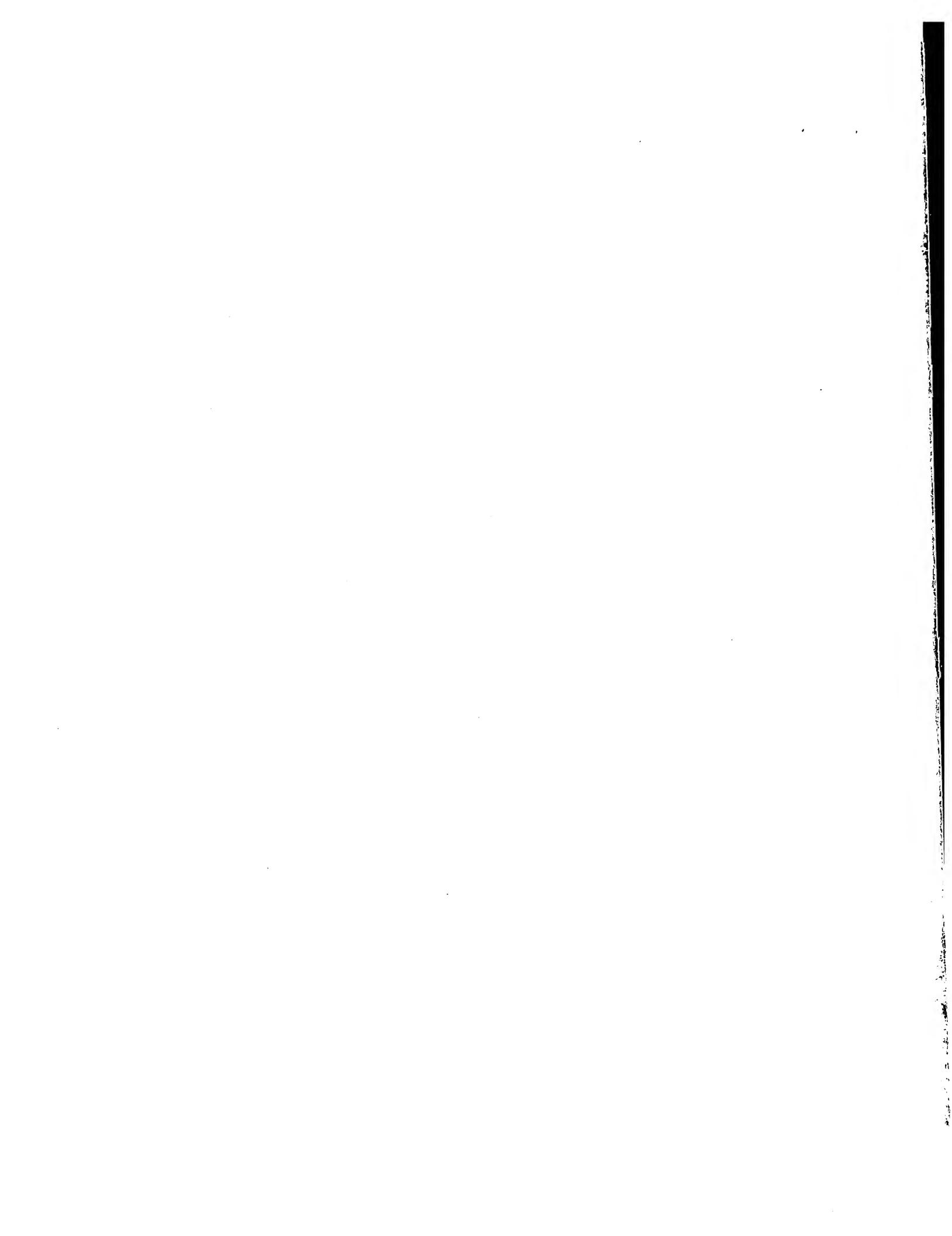
35 5. Receiver device according to claim 1, characterized in that the clock recovery unit comprises a phase locked loop (PLL) circuit (26).

40 6. Receiver device according to claim 1, characterized in that the clock recovery unit comprises a filter clock recovery circuit.

7. Data transmission system comprising an optical transmission link, in particular an optical fiber system, wherein the optical transmission link has a significant dispersion, and a receiver device according to claim 1.
- 5 8. Computer software for generating a clock signal out of an electrical data signal, in particular out of an electrical signal in the Gb/s range, wherein the electrical data signal is subjected to a frequency multiplication by a factor of n, with n being a natural number larger than 2, in particular n=4.

Abstract

A receiver device for optical data signals, in particular optical data signals in the
5 Gb/s range, comprising an opto-electrical conversion unit, a frequency
multiplicator unit (24) for frequency-multiplying the converted electrical data
signal, and a clock recovery unit is characterized in that the frequency
multiplicator unit (24) performs a frequency multiplication by a factor of n, with n
being a natural number larger than 2. The receiver device has an improved
10 tolerance on dispersion as far as generating a clock line is concerned.
(Fig. 2)



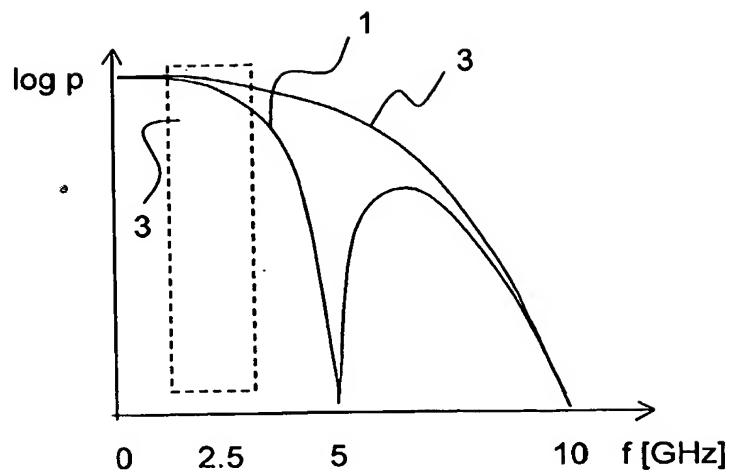


Fig. 1

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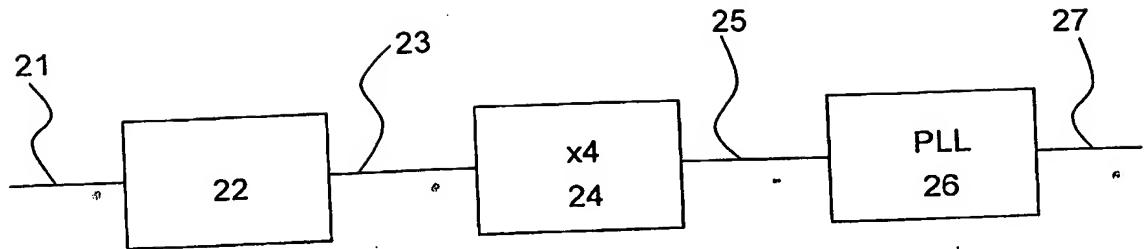


Fig. 2

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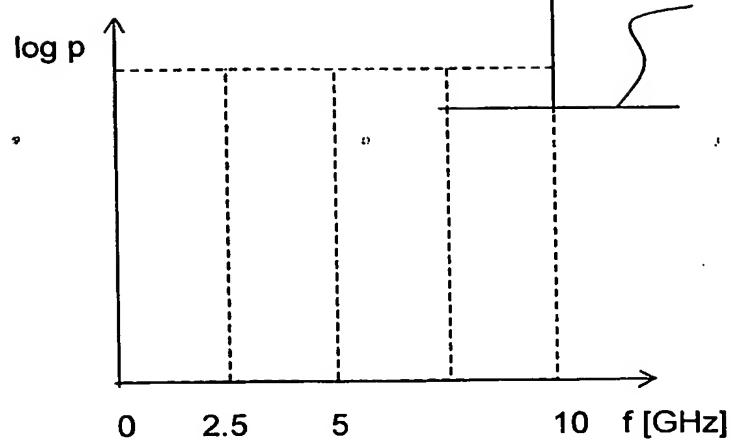


Fig. 3

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